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# 1.A shift register that will accept a parallel input or a bidirectional serial load and internal shift features is called as?

# a) Tristate b) End around c) Universal d) Conversion

# 2. The prime implicant which has at least one element that is not present in any other implicant is known as a) Essential Prime Implicant b) Implicant c) Complement d) None of the Mentioned

# 3. Why do the D flip-flops receives its designation or nomenclature as ‘Data Flip-flops’? a) Due to its capability to receive data from flip-flop b) Due to its capability to store data in flip-flop c) Due to its capability to transfer the data into flip-flop d) All of the Mentioned

# 4. Which storage element is used by MAC and IBM PC? a) CMOS b) Transistor c) Capacitor d) Inductor

# 5. By which instruction does the switching of registers take place? a) Instruction opcodes b) AXX instruction c) EXX instruction d) Register instruction

# 6. What does SPI stand for? a) serial parallel interface b) serial peripheral interface c) sequential peripheral interface d) sequential port interface

# 7. When the process is returned after an interrupt service \_\_\_\_\_\_ should be loaded again. i) Register contents ii) Condition codes iii) Stack contents iv) Return addresses a) i,iv b) ii,iii and iv c) iii,iv d) i,ii

# 8. The main memory is structured into modules each with its own address register called \_\_\_\_\_\_ a) ABR b) TLB c) PC d) IR

# 9. In a normal adder circuit the delay obtained in generation of the output is \_\_\_\_\_\_\_ a) 2n b) 2n + 2 c) n + 2 d) None of the mentioned

# 10. The periods of time when the unit is idle is called as \_\_\_\_\_ a) Stalls b) Bubbles c) Hazards d) Both Stalls and Bubbles

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| |  | | --- | |  | | 11.How is a *J-K* flip-flop made to toggle? | | |  |  | | --- | --- | | a) | *J* = 0, *K* = 0 | | b) | *J* = 1, *K* = 0 | | c) | *J* = 0, *K* = 1 | | [d)](javascript:%20void%200;) | *J* = 1, *K* = 1 | | |
| |  | | --- | |  | | 12. | Latches constructed with NOR and NAND gates tend to remain in the latched condition due to which configuration feature? | | |  |  | | --- | --- | | a) | cross coupling | | b) | gate impedance | | c) | low input voltages | | d) | asynchronous operation | | |
| |  | | --- | |  | | 13. | The output of a gated *S-R* flip-flop changes only if the: | | |  |  | | --- | --- | | a) | flip-flop is set | | b) | control input data has changed | | c) | flip-flop is reset | | d) | input data has no change | | |

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| 14. | The parallel outputs of a counter circuit represent the: |
| |  | | --- | | a)parallel data word | | b)clock frequency | | c)counter modulus | | d)clock count | |

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| 15. | Which peripheral port provides the FASTEST throughput to laser printers? |
| |  | | --- | | a)RS-232 | | b)SCSI | | c)Parallel | | d)Serial | | e)None of the above16.Conversion of decimal number 6110 to it's binary number equivalent is  |  | | --- | | a)1100112 | | b)110011102 | | c)1111012 | | d)111112 | | e)None of the above | | |

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| |  | | --- | |  | | 17. | An inverter is also called a \_\_\_\_\_ gate. | | |  | | --- | | a)NOT | | b)OR | | c)AND | | d)NAND | | e)None of the above  |  | | --- | |  | | 18. | The binary number 1100 0101 has \_\_\_\_\_ bytes | | |  | | --- | | a)1 | | b)2 | | c)4 | | d)8 | | e)None of the above19. Which among following can be considered as most advanced ROM ?a)DRAMb)EEPROMc)RAMd)PROM20. Which among following is Volatile ?a)ROMb)EPROMc)DROMd)RAM | | | | |

# Answers:

1. C
2. A
3. B
4. A
5. C
6. A
7. D
8. A
9. B
10. D
11. D
12. A
13. B
14. D
15. C
16. C
17. A
18. A
19. B
20. D

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